

31046 U.S. PTO
10/002175
12/05/01

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10002175	FILING DATE 12/05/2001	CLASS 228	SUBCLASS 180.5	GAU 1725	EXAMINER Edmondson
**APPLICANTS: Bowen Neal;					
**CONTINUING DATA VERIFIED: <i>None - LRG</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>None - LRG</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO M4065.0493/P493	
Verified and Acknowledged Examiners's Initials					
TITLE : Stacked chip connection using stand off stitch bonding					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims <input type="checkbox"/> Print Claim for O.G. <input type="checkbox"/>
ISSUE FEE		Primary Examiner	DRAWING
Amount Due	Date Paid		Sheets Drawg. <input type="checkbox"/> Figs. Drawg. <input type="checkbox"/> Print Fig. <input type="checkbox"/>
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner
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